

10039596

12/31/01

10/03/96

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10039596	FILING DATE 12/31/2001	CLASS 711	SUBCLASS 143	GAU 2186	EXAMINER M. K. Z. Li
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**APPLICANTS: David Howard; 2186

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO 42390.P13873
TITLE : Distributed memory module cache writeback		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
PREPARED FOR ISSUE		Sheets Drwg.	Figs. Drwg.
Application Examiner		Print Fig.	
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